

[illegible]

MM		MM	PPPPPPPP	
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MM		MM	DDDDDDDD	LL
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MMMM	MMMM	DD		LL
MMMM	MMMM	DD		LL
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MM		MM	DD	LL
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MM		MM	DDDDDDDD	LLLLLLLLLL

MP.MDL - Multi-processing block definitions

Version 'V04-000'

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FACILITY: Executive , Multi-processing definition macros

ABSTRACT:

This file contains the MDL source for all multi-processing
block structure definitions.

ENVIRONMENT:

n/a

AUTHOR: Kathleen D. Morse, CREATION DATE: 26-Feb-1981

MODIFIED BY:

V03-003 KDM0018 Kathleen D. Morse 13-Oct-1982
Added secondary request flag to check an event flag
wait condition.

V03-002 KDM0012 Kathleen D. Morse 20-Sep-1982
Add second error log buffer flag.

**

:+
: MPSSGL_BUGCHECK bit definitions

: This longword contains indicators for handling bugcheck.
:-

\$STRUCT BUG

V<

BUGCHK,,,M
ACK1,,,M

: Bits in MPSSGL_BUGCHECK
: Set by primary to request a bugcheck
: Set by secondary when acknowledging a bugcheck

>
E

..+ MPS\$GL_INTERLOCK bit definitions

.. This longword is accessed by interlocked instructions to flush the cache
.. so that non-interlocked accesses to other pieces of data get the correct
.. values.
..-

```
$STRUCT LCK
V<      INTERLOCK,,,M      ; Interlock bit
>
E
```


MPSSGL_STATE value definitions

This longword contains the state of the secondary processor.

\$STRUCT MPS

```
K<,$K_      IDLESTATE,1      ;Secondary states
              DROPSTATE,2      ; Idle
              BUSYSTATE,3      ; Dropping current process; CURPCB valid
              EXECSTATE,4      ; Busy; CURPCB valid but LDPCTX not
              INITSTATE,5      ; yet done
              STOPSTATE,6      ; Executing process; CURPCB valid and
                                ; LDPCTX done
                                ; Initialization uncomplete
                                ; Processor stopped
>
V<M          SECBUGCHK        ;Secondary request flags
              SECERRLOG        ; Bugcheck requested by secondary
              SECWAITCK        ; Error log requested by secondary
                                ; Event flag wait check requested by sec
>
V<M          STOPREQ          ;Bits in MPSSGL_STOPFLAG (for STOP/CPU)
              STOPACK1         ; Primary request flag
                                ; Secondary acknowledgement flag
>
V<M          ERLBUF1          ;Error log buffer flags
              ERLBUF2          ; Buffer 1 busy
                                ; Buffer 2 busy
>
K<,$K_      ERLBUFSIZ,512     ;Error log buffer information
                                ; Size of error log buffer in bytes
>
E
```

++ MPSSGL_STATE bit definitions

:- This longword records the state of the secondary processor.

\$STRUCT STA

V<

INIT...M
IDLE...M
DROP...M
BUSY...M
STOP...M

: Initialization
: Idle
: Dropping current process
: Busy executing a process
: Stopped

>
E

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MAR